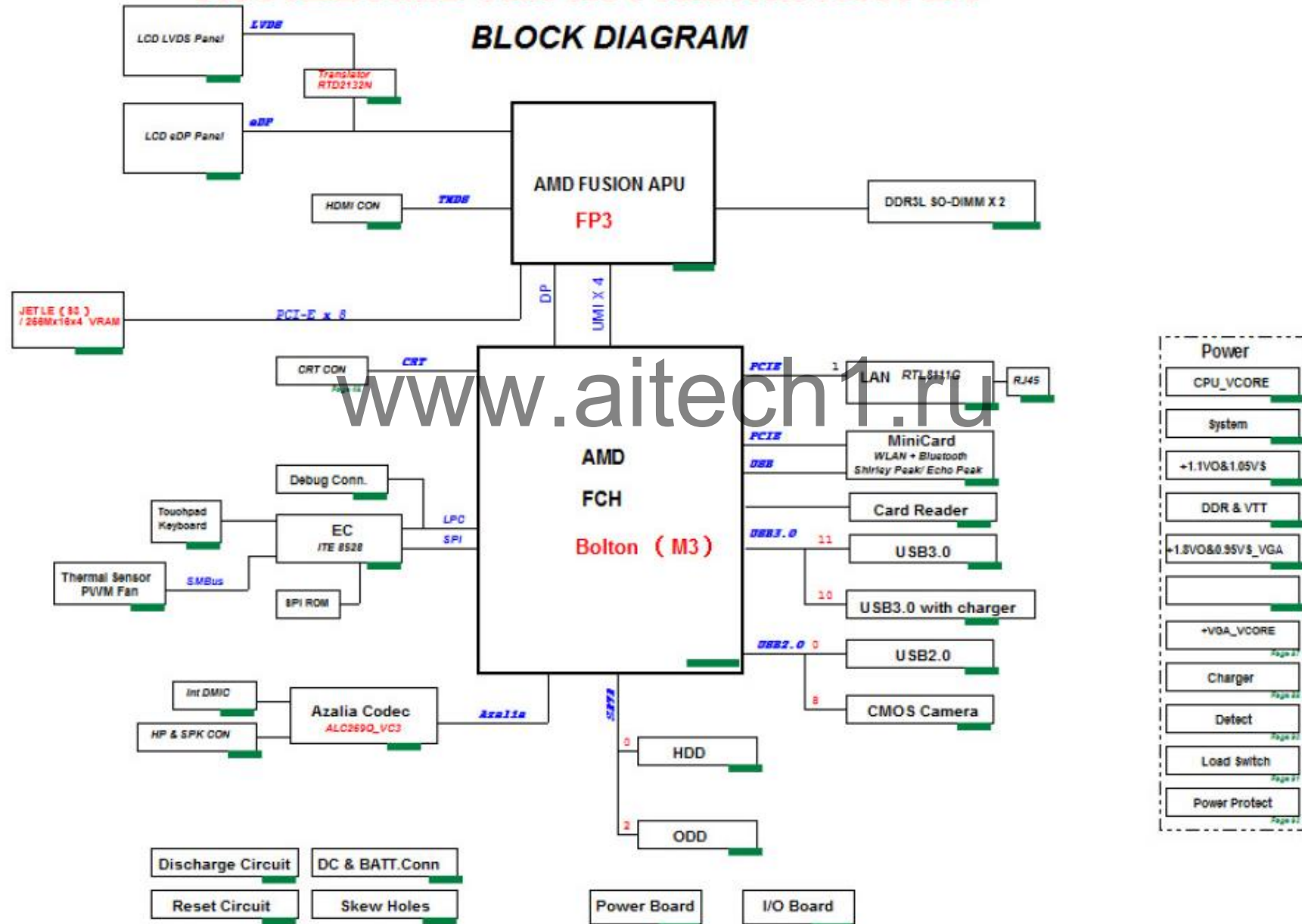
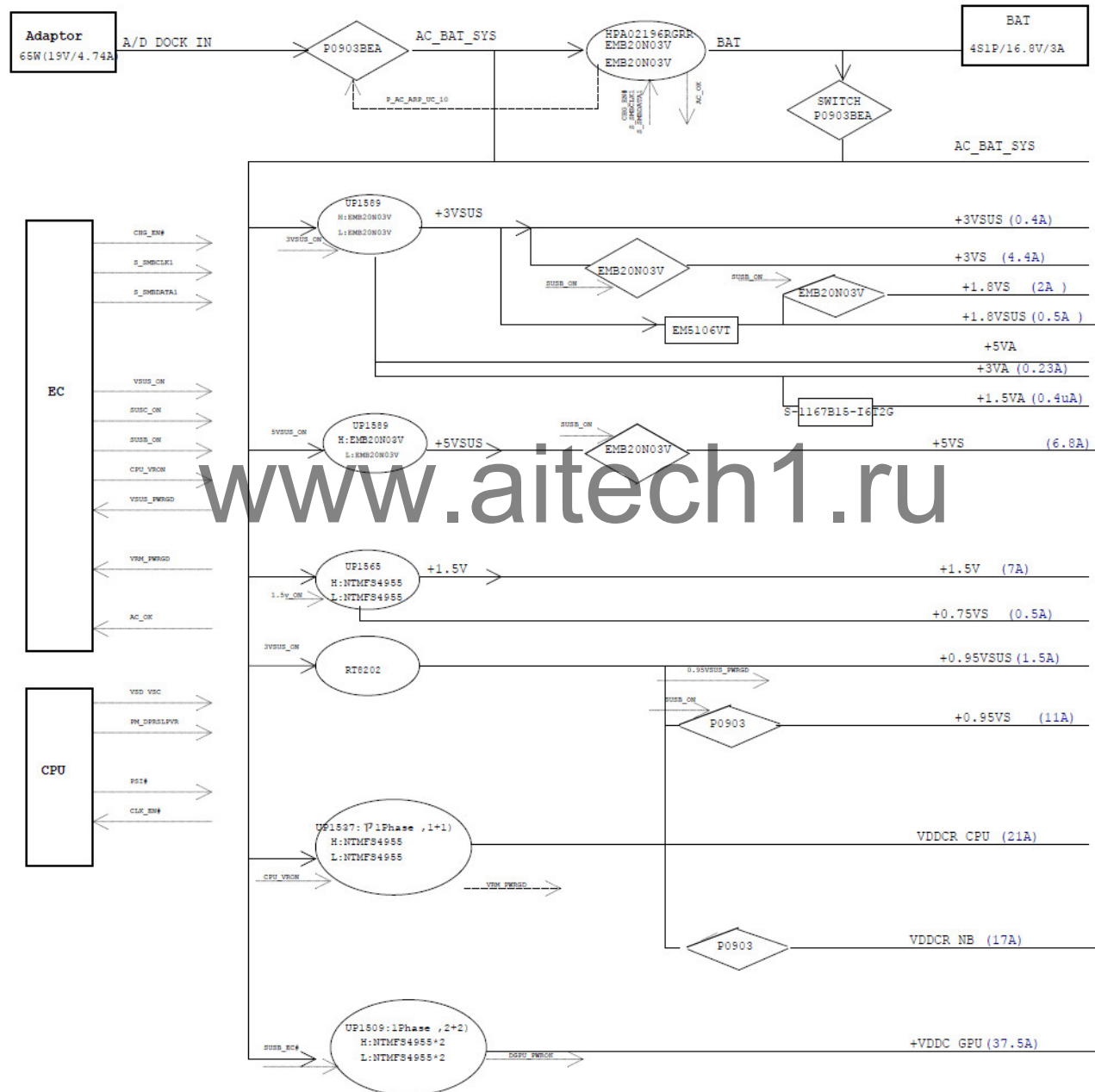


X550ZE AMD Kaveri Platform Rev. 2.0

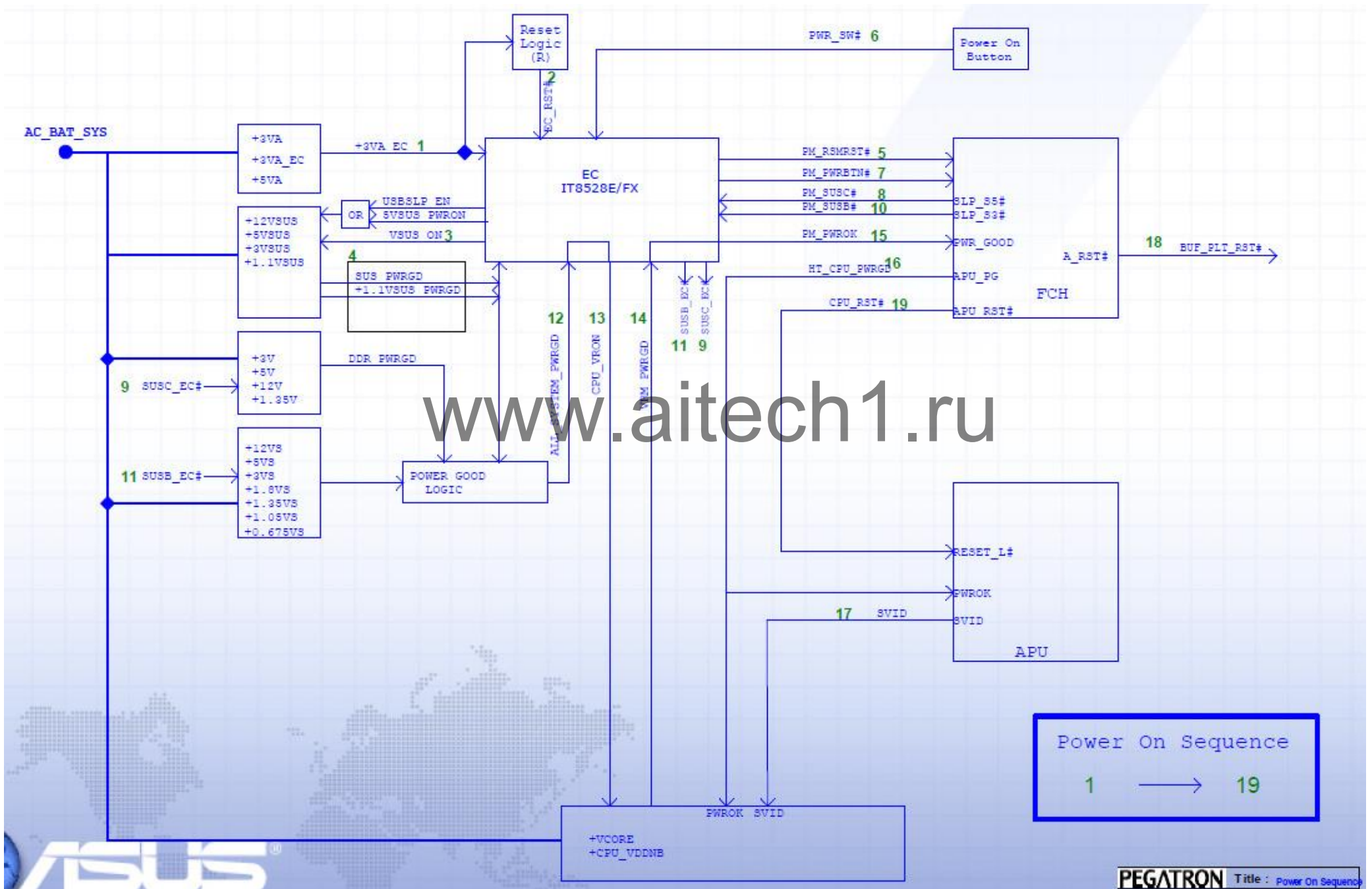
BLOCK DIAGRAM



POWER FLOW



POWER ON SEQUENCE



WLAN

14145t

13452s

L8201+1.05V0 30

L8401+1.8V0 272

18 D7001 BUF_PLT_RST# 531

L8006+V CORE 7

L8004+CPU_VDDNB 7

L8301+1.35V0 T32

Q8010 AC_BAT_SYS 435

L8701 VGA_LL1 18

L8100+5V0 387

L8101+3V0 390

5 VT787+1.1VSUS_PWRGD 476

L8200+1.1V0 170

D2011 PM_PWRBTN# 556

L8005

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LIUM BATTERY

K140410 BTG

A close-up micrograph of a CPU die. Two specific areas are highlighted with green boxes and labels: 'L8006+VCORE 7' pointing to a central circular feature, and '+CPU_VDDNB 7' pointing to a rectangular pad on the left side. The die is mounted on a green printed circuit board (PCB).

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1. R0445 CPU_RST# 383

2. C3202 +3VA_EC 456

3. C3202 EC_RST 650

4. U9104 VSUS_ON 507

5. D9202 SUS_PWRGD 476

6. R3011 PM_RSMRST# 668

7. R3016 PWR_SW# 668

8. Q5706 SUSC_EC# 578

9. R3007 PM_SUSC# 556

10. Q5705 SUSB_EC# 551

11. R3006 PM_SUSB# 556

12. R3009 CPU_VRON 556

13. D9204 ALL_SYSTEM_PWRGD 489

14. R4024 VRM_PWRGD 512

15. SR2114 PM_PWROK 489

16. R8802 AC_IN_OC# 480

17. SP0407 CPU_PWRGD 383

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